68376 BBR CODE (Boot-Block-Recovery) Release Notes:

BBR v1.03 02-Feb-2001 LWIL

- Improved the BBR code’s general Fatal Exception Handler to print out the exception type, exception vector, and a register dump (all in hex).
- Fixed a problem exhibited on REV F PDB boards. They would not go into BBR code for downloading of recovery code. The problem was traced to a spurious interrupt occuring, because the 68376 CPU’s Port F Pin Assignment register was not being initialized. This caused random/spurious external interrupts to occur as soon as the CPU’s interrupt mask was changed to allow interrupts.
- Fixed a potential DF1 re-entrancy bug. May have caused lost or corrupted outgoing DF1 packets. This problem has not actually been experienced.
- Built against the baseline #2030.