3761BC1
3761BC1.SCH

3761BC2
3761BC2.SCH

Notes:

1. Route current sense traces away from power and digital grounds.
2. Star ground current sense resistors, bypass capacitors, and RC filter network. Common analog and power grounds at this point.
3. Minimize loop areas contained by each switching cell (high and low side transistors, bypass capacitors, and current sense resistors).
4. Minimize loop areas contained by gate drive circuits.
5. Make bootstrap diode VDD connections close to HIP4080 bypass capacitors.
6. High side gate drive ground and power planes are galvanically isolated from each other and from all other ground and power planes. Connect power planes as noted.
7. Unless otherwise noted, swap pins and gates to minimize trace lengths.
8. Place labels adjacent to connectors as noted.

CREO

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DESIGNED BY:	DOCUMENT NUMBER:	LAST ECO:
G. CUMMINGS	10-3761B-C	D075
DRAWN BY:	LAST REVISED BY:	APPROVED BY:
G. CUMMINGS	G. CUMMINGS	G. CUMMINGS
DESIGN DATE:	LAST MODIFIED DATE:	SHEET:
MARCH 16, 1998	AUGUST 03, 1999	1 of 3