I:\NG\israel_o\MG2002EE\power_control_pcb\schemat.pc\SH1 Last update : 08/10/96 ARCHIVE

REVISIONS

LTR	ECO. NO	DATE	APPROVED	LTR	ECO. NO	DATE	APPROVED
A	20245	25/01/95	YUVAL				
B	20731	20/02/95	YUVAL				
C	20920	30/03/95	YUVAL				
D	21325	21/02/96	PULI				
E	22773	08/10/96	PULVERS				

REVISION STATUS OF SHEETS

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22
E	D	D																			
23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42		
43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62		

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SCITEX

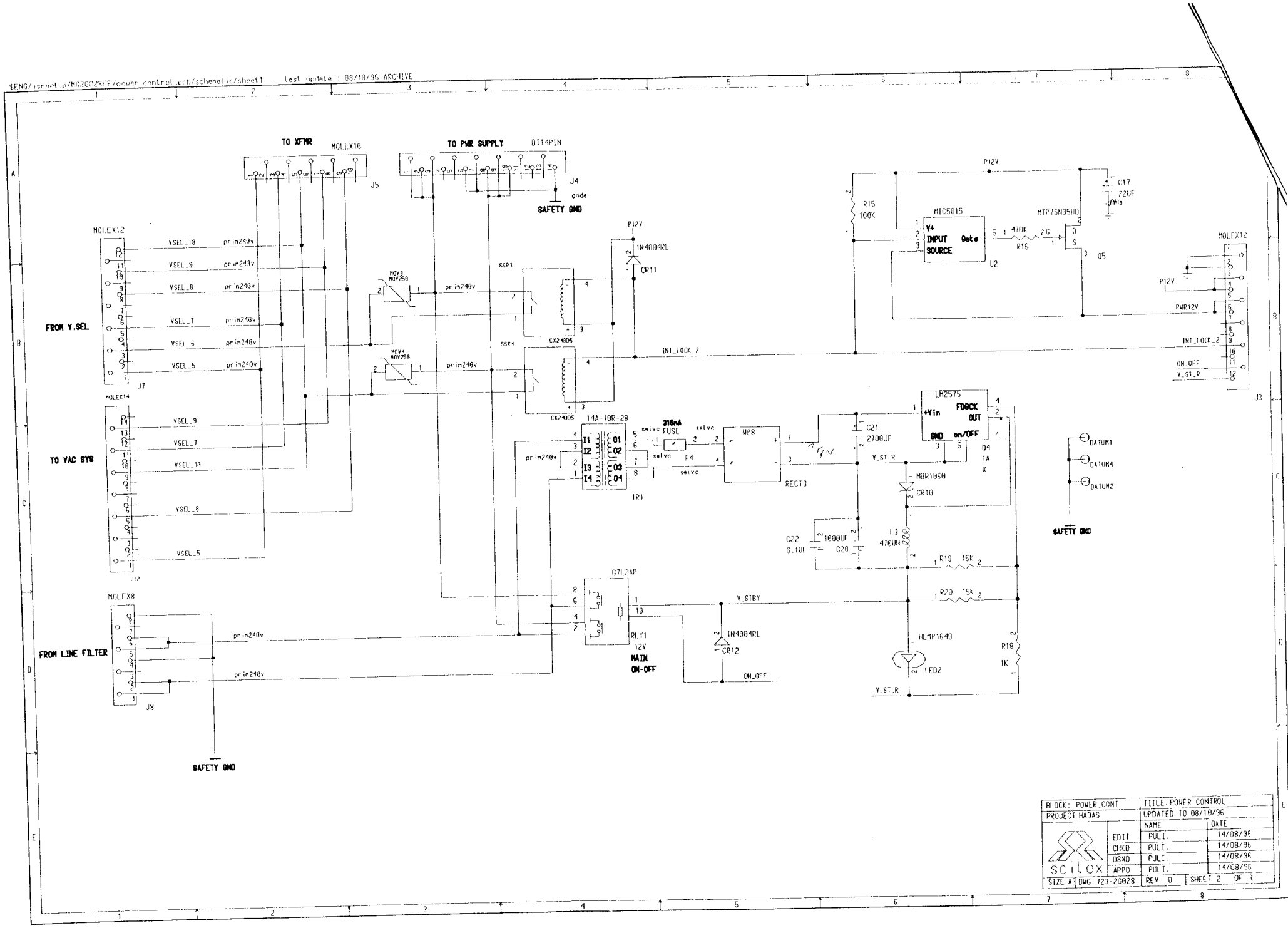
APPROVAL SIGNATURES	
EDIT	PULI.
CHECKED	PULI.
DESIGNED	PULI.
APPROVED	PULI.
DATE	14/08/96

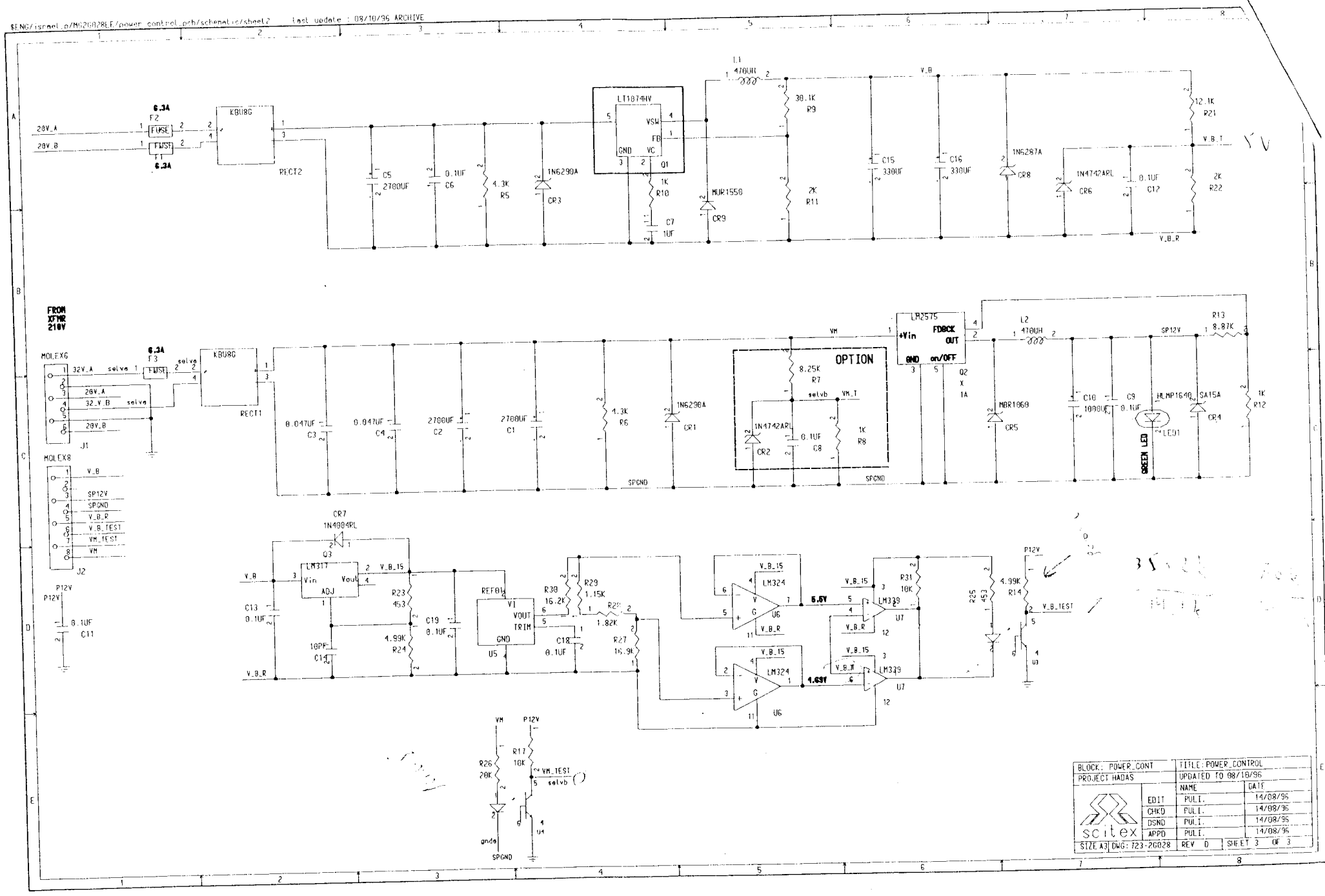
TITLE: POWER_CONTROL

PROJECT: HADAS

SHEET 1 OF 3

SIZE	DWG NO.:	REV:
A3	723-26028	E


$ENG/schpet_p/MG2G028E/power_control_pcb/schematic/sheet1 Last update : 08/10/96 ARCHIVE
TO XFMR
MOLEX10
J5
TO PWR SUPPLY
DI14PIN
J4
SAFETY GND
MOLEX12
FROM V.SEL
VSEL_10
VSEL_9
VSEL_8
VSEL_7
VSEL_6
VSEL_5
J7
MOLEX14
TO VAC SYS
VSEL_9
VSEL_7
VSEL_10
VSEL_8
VSEL_5
J12
MOLEX8
FROM LINE FILTER
J8
SAFETY GND
prin240v
MOV3 MOV250
MOV4 MOV250
SSR3
SSR4
CX2405
P12V
1N4004RL
CR11
INT_LOCK_2
14A-10R-28
I1
I2
I3
I4
O1
O2
O3
O4
IR1
316mA FUSE
F4
selvc
W08
RECT3
LM2575
+Vin
FDBCK
OUT
GND
on/OFF
Q4
1A
X
C21
2700UF
V_ST_R
MBR1060
CR10
C22
0.1UF
C20
1000UF
L3
470UH
R19
15K
R20
15K
R18
1K
G7L2AP
RLY1
12V
MAIN
ON-OFF
1N4004RL
CR12
ON_OFF
HLMP1640
LED2
MIC5015
V+
INPUT
SOURCE
Gate
U2
MTP75N05HD
Q5
R15
100K
R16
470K
C17
.22UF
PWR12V
DATUM1
DATUM4
DATUM2
SAFETY GND
BLOCK: POWER_CONT
PROJECT HADAS
TITLE: POWER_CONTROL
UPDATED TO 08/10/96
NAME
DATE
EDIT PULI. 14/08/96
CHKD PULI. 14/08/96
DSND PULI. 14/08/96
APPD PULI. 14/08/96
scitex
SIZE A1 DWG: 723-2G028
REV D
SHEET 2 OF 3


$ENG/israel_p/MH2G028LE/power_control_pcb/schematic/sheet2 Last update : 08/10/96 ARCHIVE
28V_A
28V_B
F2
6.3A
FUSE
F1
6.3A
KBU8G
RECT2
C5
2700UF
C6
0.1UF
4.3K
R5
1N6290A
CR3
LT1074HV
VSW
FB
GND
VC
Q1
1K
R10
C7
1UF
L1
470UH
MUR1550
CR9
30.1K
R9
2K
R11
V_B
C15
330UF
C16
330UF
1N6287A
CR8
1N4742ARL
CR6
0.1UF
C12
12.1K
R21
V_B_T
2K
R22
V_B_R
FROM XFMR 210V
MOLEX6
32V_A
selva
F3
6.3A
FUSE
28V_A
32_V_B
selva
28V_B
J1
KBU8G
RECT1
0.047UF
C3
0.047UF
C4
2700UF
C2
2700UF
C1
4.3K
R6
1N6290A
CR1
SPGND
VM
OPTION
8.25K
R7
selvb
VM_T
1N4742ARL
CR2
0.1UF
C8
1K
R8
SPGND
LM2575
+Vin
FDBCK
OUT
GND
on/OFF
Q2
X
1A
L2
470UH
MBR1060
CR5
SP12V
R13
8.87K
C10
1000UF
C9
0.1UF
HLMP1640
LED1
GREEN LED
SA15A
CR4
1K
R12
MOLEX8
V_B
SP12V
SPGND
V_B_R
V_B_TEST
VM_TEST
VM
J2
P12V
P12V
0.1UF
C11
CR7
1N4004PL
Q3
LM317
Vin
Vout
ADJ
V_B
V_B_15
R23
453
4.99K
R24
10PF
C14
C13
0.1UF
C19
0.1UF
REF01
VI
VOUT
TRIM
GND
U5
R30
16.2K
R29
1.15K
R28
1.82K
C18
0.1UF
R27
16.9K
V_B_15
LM324
U6
5.5V
4.63V
V_B_R
LM339
U7
R31
10K
R25
453
4.99K
R14
V_B_TEST
U3
VM
P12V
R17
10K
R26
20K
VM_TEST
selvb
gnde
SPGND
U4
BLOCK: POWER_CONT
PROJECT HADAS
TITLE: POWER_CONTROL
UPDATED TO 08/10/96
NAME
DATE
EDIT
PULI.
14/08/96
CHKD
PULI.
14/08/96
DSND
PULI.
14/08/96
APPD
PULI.
14/08/96
scitex
SIZE A3
DWG: 723-2G028
REV D
SHEET 3 OF 3