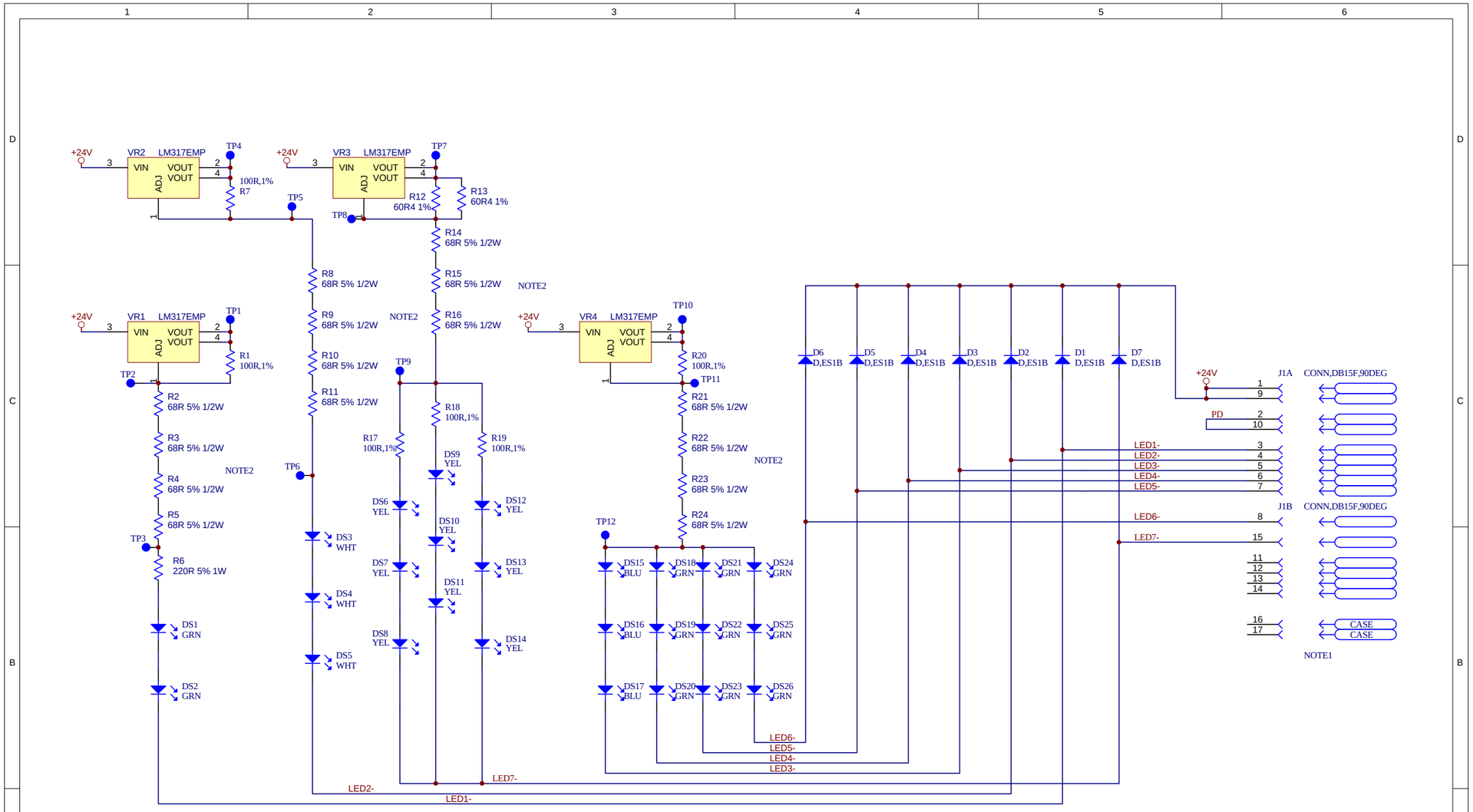


Note:

1) Place the connector J1 on the bottom side of the board and at the edge of the board as shown in components placement drawing.
2) 1/2 W resistors placed to meet Class 2 radiation emission standards(signal fault cannot lead to hazardous condition, in this case the LED's being too hot to burn out)
3) Place all LEDs DS1 to DS27 on top side of the board and at the exact locations as shown in components placement drawing.
4) Place the rest of components(VR1 to VR4, R1 to R24 and D1 to D7). on the top side of the board as well, but not in the keep-out area as shown in the components placement drawing, as the area will be some dividers there to separate different colors of LEDs.
5) Place 12 test pads on the bottom side of the board, same side as J1.

Error : CREOLOGO.BMP file not found.

BRD,ASSY,USER INTERFACE SSR, MTA

C:\Projects\Manta\SSR\SSR_UI~1.DDB - Documents\SSR_UI~1.SCH

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2003:06:17	2003:07:15	1 of 1