Brisque Multidevice Configuration

Brisque supports up to 2 Proofs (Veris/Iris), 4 imposition proofing/Integris (TCPIP only) and 2 screening devices (CTP or CTF or Halftone or Screen2Go) on a single SMP server.
The table describes the maximum supported devices on each Brisque platform for parallel processing.

Device (up to)			TSP	Brisque Platform																							
Proof	Improof	Screening		E30		Tiger I (43P-		Tiger II (43P-		Performance Single		Brisque2 (F50 2-		Brisque4 (F50 4-		Performance Dual (6F1 2-		Performance Quad (6F1 4-		Performance Sixt (6F1 6-		Express Single (6E3 1		Express Dual (6E3 2-CPU)		Express Quad (6E4 4-	
				v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5	v4.1	v5
1	1	1 CTF	1	√	N/A	√	N/A	√	√	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
1	2	1 CTF	1	X	N/A	X	N/A	√	√	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
1	2	1 CTP	1	X	N/A	X	N/A	√	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
1	2	1 S2G	1	X	N/A	X	N/A	√	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
1	2	1 Halftone	1	X	N/A	X	N/A	√	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	2	1 CTF	1	X	N/A	X	N/A	X	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	2	1 CTP	1	X	N/A	X	N/A	X	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	2	1 S2G	1	X	N/A	X	N/A	X	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	2	1 Halftone	1	X	N/A	X	N/A	X	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	2	1 Fast CTP	2	X	N/A	X	N/A	X	X	√	√	√	√	√	√	√	√	√	√	√	√	N/A	√	N/A	√	N/A	√
2	4	1 CTF	1	X	N/A	X	N/A	X	X	X	X	√	√	√	√	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTP	1	X	N/A	X	N/A	X	X	X	X	√	√	√	√	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 S2G	1	X	N/A	X	N/A	X	X	X	X	√	√	√	√	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 Halftone	1	X	N/A	X	N/A	X	X	X	X	√	√	√	√	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	2 Kodak XP4	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTF, 1 CTP	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	serial	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTF, 1 S2G	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	serial	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTP, 1 S2G	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	serial	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTF, 1 Halftone	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 CTP, 1 Halftone	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 S2G, 1 Halftone	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 Fast CTP	2	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√
2	4	1 Fast CTF	1	X	N/A	X	N/A	X	X	X	X	√	serial	√	serial	√	√	√	√	√	√	N/A	X	N/A	√	N/A	√

By Device Type:

Type	Device Name
CTF	Dolev (all models except Dolev800V[3])
CTP	Trendsetter Quantum S & F speed, Trendsetter Quantum Spectrum
S2G	Screen2Go
Halftone	ProofSetter, Kodak XP4
Fast CTP	Lotem Quantum (& II), Lotem Quantum (& II), Spectrum, Trendsetter Quantum (& II) V speed
Fast CTF	Dolev800V[3]
Proof	IrisPrint, IrisRealist, Veris
ImProof	HP, IrisWide, Epson

By Device Name

Device Name	Type
Dolev (all models except Dolev800V[3])	CTF
Dolev800V[3]	Fast CTF
Lotem	CTP
Lotem Quantum V speed (& II)	Fast CTP
Lotem Quantum Spectrum (& II)	Fast CTP
Trendsetter, Trendsetter Spectrum, Trendsetter Quantum	CTP
Trendsetter Quantum Spectrum	CTP
Trendsetter Quantum V speed (& II)	Fast CTP
ProofSetter	Halftone
Kodak XP4	Halftone
Screen 2 Go	S2G

Notes:

- One TSP board for S, F & P speed devices and Dolev800V[3].
- Two TSP boards for V speed devices.
- Improof - only TCP/IP configuration is supported.
- SFL & VLSI interface board are not supported in multidevice configurations. Only TSP board is supported.
- Dolev 200/400 cannot be part of multidevice configurations.
- Dolev800V[3] is supported by TSP100 only.
- It is possible to expose to CTP in parallel to Screen2Go, using two TSP boards in SMP configurations.
- One TSP board used for both operations, S2G and CTP or CTF is supported. The operator is responsible for The synchronization of The operations.
- Screen2Go is supported by both TSP75 and TSP100.
- For F50 platforms (Brisque2 & Brisque4 configurations), 2 screening devices in parallel process is supported only on v4.1. For v5, only serial processes are allowed in order to ensure the performance.
- **Full engine speed is not guaranteed for any parallel device configuration. For full engine speed for each CTP per Brisque platform, see the Platform_CTP_Devices matrix**

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