Brisque V4.1 Parallel-Device configurations

May 2003

Devices, up to:				Brisque Platforms								
Proof	**Improof**	**Screen devices**	**TSP board quantity**	**E30**	**Tiger I 43P-140**	**Tiger II 43P-150**	**Hammerhead 44P-170**	**F50/ 2 CPU**	**F50/ 4 CPU**	**6F1/ 2 CPU pSeries**	**6F1/ 4 CPU pSeries**	**6F1/ 6 CPU pSeries**
1	1	1 CTF	1	√	√	√	√	√	√	√	√	√
1	2	1 CTF	1	X	X	√	√	√	√	√	√	√
1	2	1 CTP	1	X	X	√	√	√	√	√	√	√
1	2	1 S2G	1	X	X	√	√	√	√	√	√	√
1	2	1 HalfTone	1	X	X	√	√	√	√	√	√	√
2	2	1 CTF	1	X	X	X	√	√	√	√	√	√
2	2	1 CTP	1	X	X	X	√	√	√	√	√	√
2	2	1 S2G	1	X	X	X	√	√	√	√	√	√
2	2	1 HalfTone	1	X	X	X	√	√	√	√	√	√
2	2	1 Fast CTP [3]	2	X	X	X	√	√	√	√	√	√
2	4	1 CTF	1	X	X	X	X	√	√	√	√	√
2	4	1 CTP	1	X	X	X	X	√	√	√	√	√
2	4	1 S2G	1	X	X	X	X	√	√	√	√	√
2	4	1 HalfTone	1	X	X	X	X	√	√	√	√	√
2	4	2 Kodak XP4	2	X	X	X	X	√	√	√	√	√
2	4	1 CTF, 1 CTP	2	X	X	X	X	√	√	√	√	√
2	4	1 CTF, 1 S2G	2	X	X	X	X	√	√	√	√	√
2	4	1 CTP, 1 S2G	2	X	X	X	X	√	√	√	√	√
2	4	1 CTF, 1 HalfTone	2	X	X	X	X	√	√	√	√	√
2	4	1 CTP, 1 HalfTone	2	X	X	X	X	√	√	√	√	√
2	4	1 S2G, 1 HalfTone	2	X	X	X	X	√	√	√	√	√
2	4	1 Fast CTP [3]	2	X	X	X	X	√	√	√	√	√
2	4	1 Fast CTF [3, 7]	2	X	X	X	X	√	√	√	√	√

By Device group:

CTF	**Dolev** (all models except Dolev800V^3)
CTP	**Lotem, TrendSetter, TrendSetter Spectrum, TrendSetter Quantum S & F speed, TrendSetter Quantum Spectrum**
S2G	**Screen 2 Go**
HalfTone	**ProofSetter, Kodak XP4**
Fast CTP	**Lotem Quantum (& II), Lotem Quantum (& II) Spectrum, TrendSetter Quantum (& II) V speed** [3]
Fast CTF	**Dolev800V^3** [7]
Proof	**IrisPrint, IrisRealist, Veris**
ImProof	**HP, IrisWide, Epson**

By Device type:

Dolev (all models except Dolev800V^3)	CTF
Dolev800V^3 [7]	Fast CTF
Lotem	CTP
Lotem Quantum V speed (& II)	Fast CTP
Lotem Quantum Spectrum (& II)	Fast CTP
TrendSetter, TrendSetter Spectrum, TrendSetter Quantum S & F speed	CTP
TrendSetter Quantum Spectrum	CTP
TrendSetter Quantum V speed (& II) [3]	Fast CTP
ProofSetter	HalfTone
Kodak XP4	HalfTone
Screen 2 Go	S2G

Notes:

3. **One TSP** board for **S, F** & **P** speed devices and Dolev800V^3.
   Two TSP boards for **V** speed devices.
4. **Improof -** only TCP/IP configuration is supported.
5. **SFL & VLSI** interface board is not supported in multi-device configurations. Only **TSP** board is supported.
   i.e. **Dolev 200/400** cannot be part of multi-device configurations.
7. Dolev800V^3 is supported by **TSP100**.
8. **Screen 2 Go -** it is possible to perform CTP in parallel to Screen 2 Go, using two TSP boards in SMP configurations.
   - Serial operation (using one TSP board for both operations, S2G and CTP or CTF) is supported. The operator is responsible for the synchronization of the operations.
   - Screen 2 Go is supported by both TSP75 and TSP100.
9. **Full engine speed is not guaranteed for any parallel devices configuration.**